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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10068148	02/06/2002	365		2818	A. J. GOO

**APPLICANTS: Lee Jae Goo.

**CONTINUING DATA VERIFIED:
NONE.

** FOREIGN APPLICATIONS VERIFIED:
REPUBLIC OF KOREA 2001-7276 02/14/2001

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no

35 USC 119 conditions met ☒ yes ☐ no

Verified and Acknowledged Examiners's initials *fg*

ATTORNEY DOCKET NO

5649-952

TITLE : Integrated circuit memory devices with per-bit redundancy and methods of operation thereof

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs.Drwg. Print Fig.
Primary Examiner		Application Examiner	
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